

FORM PTO-1449 <u>INFORMATION DISCLOSURE STATEMENT</u>	MMB DOCKET NO. 1890-0030	APPLICATION NO.: To be assigned 10/748711
	APPLICANT(S): Brox et al.	
	FILING DATE: Herewith 12/30/03	GROUP ART UNIT: To be assigned 2816

U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE
AK	AA	5,604,775	February 18, 1997	Saitoh et al.	375	376	
AK	AB	6,326,826	December 4, 2001	Lee et al.	327	161	
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
AK	AL	DE 19930167 A1	January 18, 2001	Germany			Yes No
	AM						Yes No
	AN						Yes No
	AO						Yes No
	AP						Yes No

OTHER (Including Author, Title, Date, Pertinent Pages, etc.)			
AK	AQ	1	Atsushi Hatakeyama et al. "A 256-Mb SDRAM Using a Register Controlled Digital DLL," IEEE Journal of Solid-State Circuits, Vol 32, No 11, ©1997, pages 1728-1734, (7 pages).
	AR	1	
	AS	1	

EXAMINER LUU	DATE CONSIDERED 1/12/05
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicants.